





Directly connect PCIe SSD without external memory!!



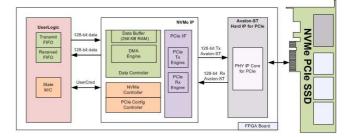


Evaluation on Intel® Arria® 10 SX FPGA Development Kit with Intel® NVMe PCIe SSD

NVMe IP core interfaces Ultra high-speed PCIe SSD without CPU and external memory. It is the best solution for applications which require ultra high-speed performance with compact system. The IP core license includes the reference design for Intel FPGA boards to shorten development time and reduce the cost.

Free evaluation sof files for Intel FPGA boards are available. You can evaluate IP core performance before purchasing.

Block diagram



<u>Features</u>

- Implement application layer to access PCIe SSD without CPU and external memory
- Support PCIe Gen3, theoretical upper limit 4GB/sec
- Small resource, the best solution for building a compact system
- FAT32 access without CPU * with optional FAT32-IP
- Free evaluation before purchasing

Reference Designs are available for practical applications



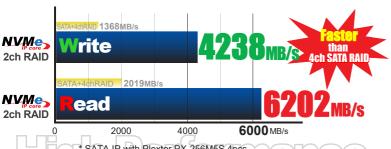
Easy to apply for high-end products such as ultra high-speed data recorder



Suitable for high-speed data recording and stand-alone data analysis on SoC

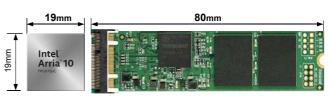
<u> Performance/Application</u>

Able to build Gen3 PCIe SSD 2ch RAID system!



SATA IP with Plextor PX-256M5S 4pcs NVMe IP with Samsung 960Pro 2pcs

The best solution for Compact Ultra High-speed storage system!!



Arria® 10 SX(UBGA484) NVMe IP + user logic

M.2 SSD for data storage

System space image by 484pin UBGA package FPGA with M.2 SSD

<u> Product, Line, up</u>

IP core NVMe-IP-A10SX 1 project Netlist License for Arria® 10 SX NVMe-IP-A10GX 1 project Netlist License for Arria® 10 GX NVMe-IP-A5GX 1 project Netlist License for Arria® V GX NVMe-IP-FAT32-A FAT32 file system for NVMe-IP. Purchase with NVMe-IP core

Please ask us about Multi-License, Evaluation License and Maintenance support License

Accessories for evaluation

AB16-PCIeXOVER

PCIe Crossover Adapter board for NVMe IP evaluation



For more detail and technical information on our web site http://www.dgway.com/NVMe-IP_A_E.html





