

SDLink Support Device List			29-May-18	
Vendor	DeviceFamily	Serial Mode (Passive-Serial)	Parallel Mode (Fast Passive-Parallel)	
Altera	Stratix V (*3,*4)	3.0V or 2.5V	3.0V or 2.5V or 1.8V	
	Stratix IV (*3)	3.0V or 2.5V	3.0V or 2.5V or 1.8V	
	Stratix III	3.3V or 2.5V	3.3V or 2.5V or 1.8V	
	Stratix II (*1)	3.3V or 2.5V	3.3V or 2.5V	
	Stratix(*2)	3.3V or 2.5V	3.3V or 2.5V	
	Arria10 GX, SX, GT (*3,*4)	1.8V	1.8V	
	Arria V GX,GT,SX,ST (*3,*4)	3.3V or 3.0V or 2.5V or 1.8V	3.3V or 3.0V or 2.5V or 1.8V	
	Arria V GZ (*3,*4)	3.0V or 2.5V or 1.8V	3.0V or 2.5V or 1.8V	
	Cyclone V (*3,*4)	3.3V or 3.0V or 2.5V or 1.8V	3.3V or 3.0V or 2.5V or 1.8V	
	Cyclone IV (*3)	3.3V or 2.5V	(Not supported)	
	Cyclone III	3.3V or 2.5V	3.3V or 2.5V or 1.8V	
	Cyclone II	3.3V or 2.5V	(Not supported)	
	Cyclone	3.3V or 2.5V	(Not supported)	
	(*1) Stratix II GX is not supported yet			
	(*2) Stratix GX is not supported yet			
(*3) Fast POR Delay mode is not supported by SDLink				
(*4) FPP data width must be 8bit				
Vendor	DeviceFamily	Serial Mode (Slave-Serial)	Parallel Mode (Slave-Parallel [SelectMAP8])	
Xilinx	Virtex UltraScale+ and Kintex UltraScale+	1.8V (*14)	1.8V (*14)	
	Virtex UltraScale and KU095	3.3V or 2.5V or 1.8V (*14)	1.8V (*14)	
	Kintex UltraScale except KU095	3.3V or 2.5V or 1.8V (*14)	3.3V or 2.5V or 1.8V (*14)	
	Kintex-7	3.3V or 2.5V or 1.8V (*13)	3.3V or 2.5V or 1.8V (*13)	
	Artix-7	3.3V or 2.5V or 1.8V (*13)	3.3V or 2.5V or 1.8V (*13)	
	Virtex-7	1.8V	1.8V	
	Virtex-6	2.5V or 1.8V	2.5V or 1.8V (*12)	
	Virtex-5	3.3V or 2.5V or 1.8V	3.3V or 2.5V or 1.8V (*11)	
	Virtex-4	3.3V or 2.5V or 1.8V	3.3V or 2.5V or 1.8V (*11)	
	Spartan-6	2.5V or 1.8V	2.5V or 1.8V	
	Spartan-3E	3.3V or 2.5V	3.3V or 2.5V	
	Spartan-3A	3.3V or 2.5V	3.3V or 2.5V	
	Spartan-3	3.3V or 2.5V	3.3V or 2.5V	
	(*11) For Virtex-4/5 in SelectMAP8 mode, VCCO voltage of Bank0 and Bank2 must be the same			
	(*12) For Virtex-6 in SelectMAP8 mode, VCCO voltage of Bank0 and Bank24 must be the same			
(*13) For Kintex-7 and Artix-7, VCCO voltage of Bank0 and Bank14 must be the same				
(*14) For Kintex UltraScale(+) and Virtex UltraScale(+), VCCO voltage of Bank0 and Bank65 must be the same				
Remark: Following features are not supported by SDLink				
Data compression				
Design security (Bit Stream Encryption)				
Remote system update				