

VariClock Output Waveform

Ver1.1

This application note shows the measured clock signal waveform of VariClock.

[1] Condition

Oscilloscope: TDS3054 from Tektronix

Bandwidth: 500MHz

Sampling Rate: 5Gsample/sec

Oscilloscope Probe: P6139A from Tektronix

Bandwidth: 500MHz

Load Impedance: 8.0pF / 10Mohm

Tested Board:

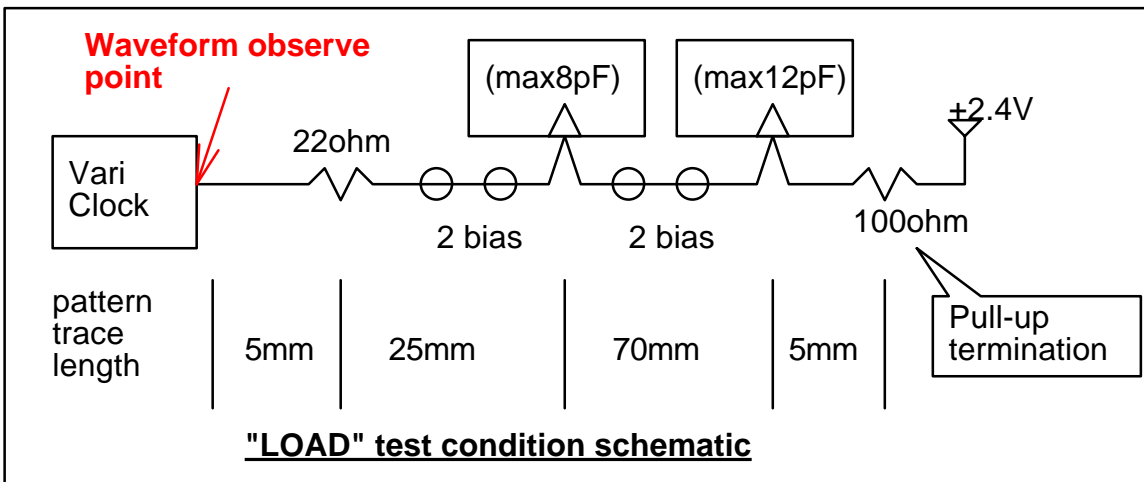
Power Supply: +5.0V Vcc

Board type: Grass Epoxy, 4layer board

Tested Condition:

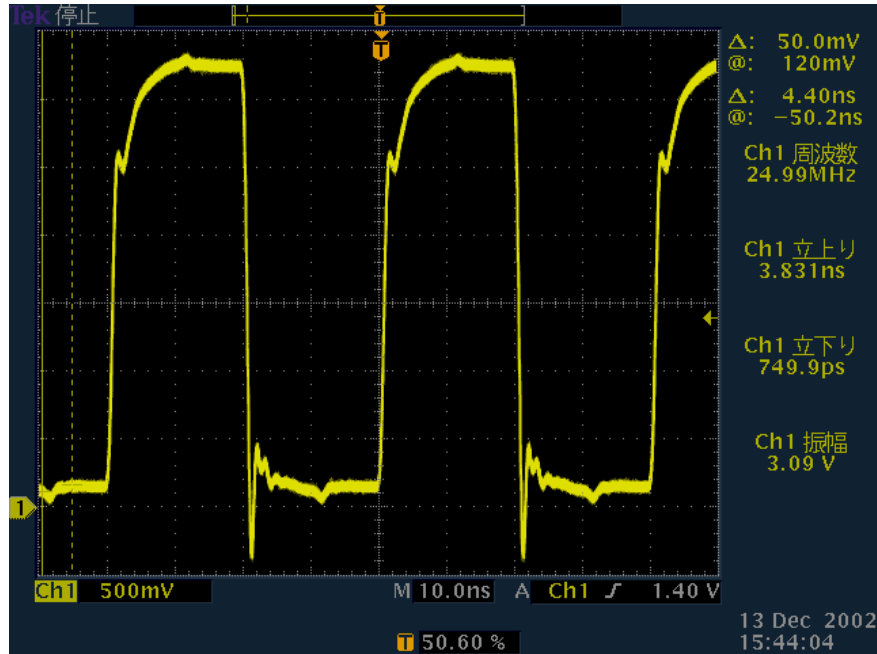
[A] No Load: Completely no load except Probe of Oscilloscope

[B] With Load: With load of below schematics & Probe of Oscilloscope

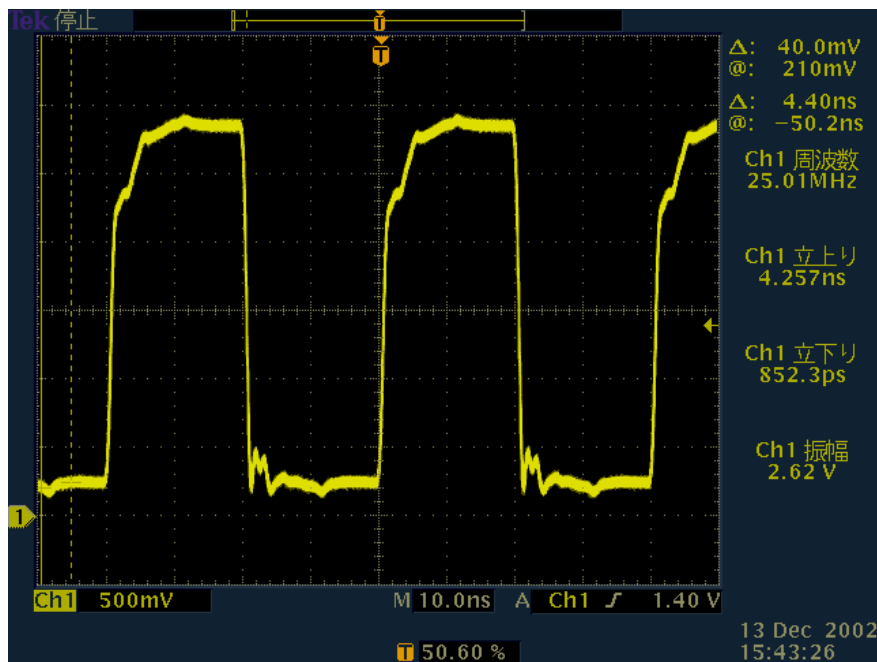


[2] Measured Waveform

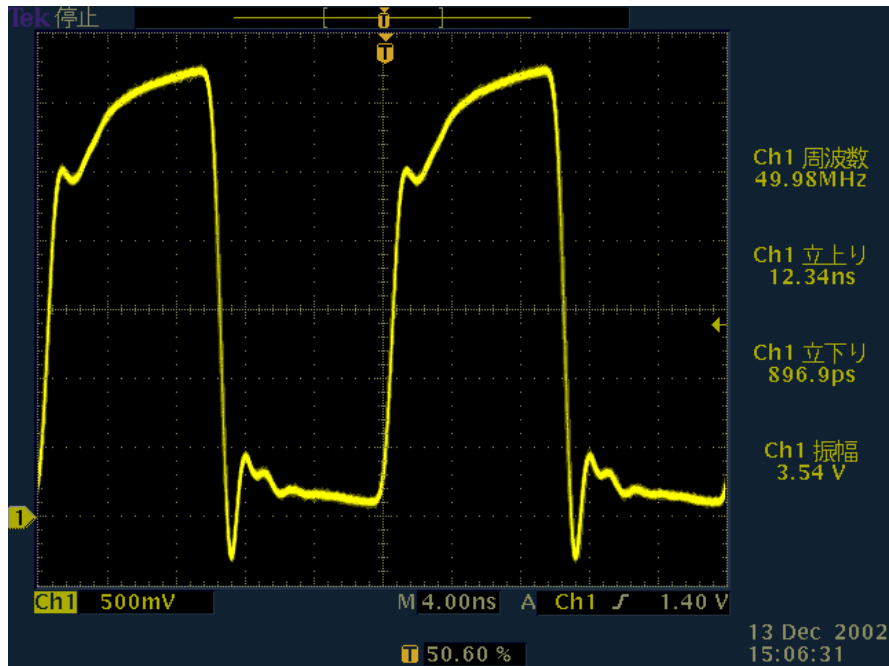
[2-1] 25MHz



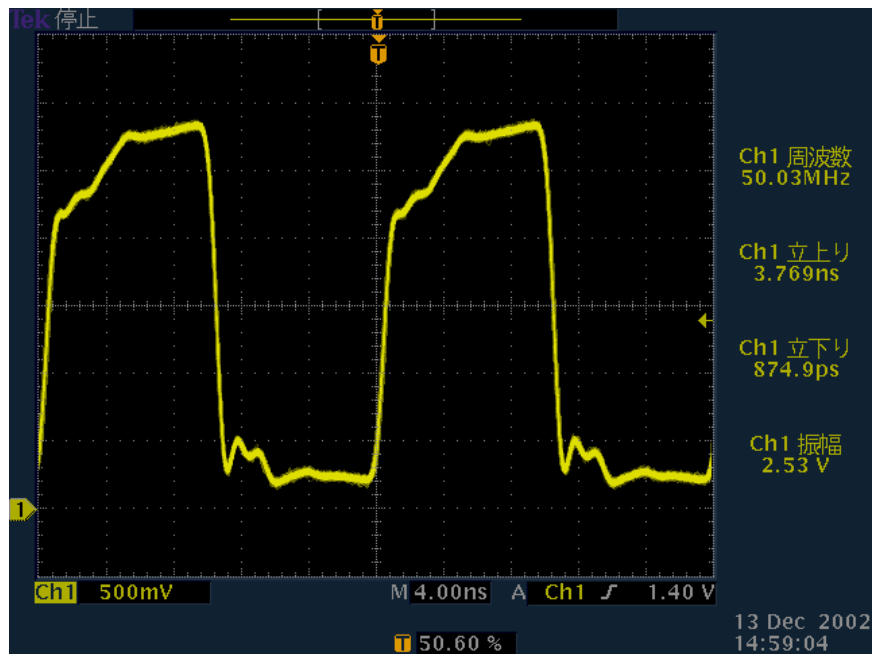
No Load @ 25MHz



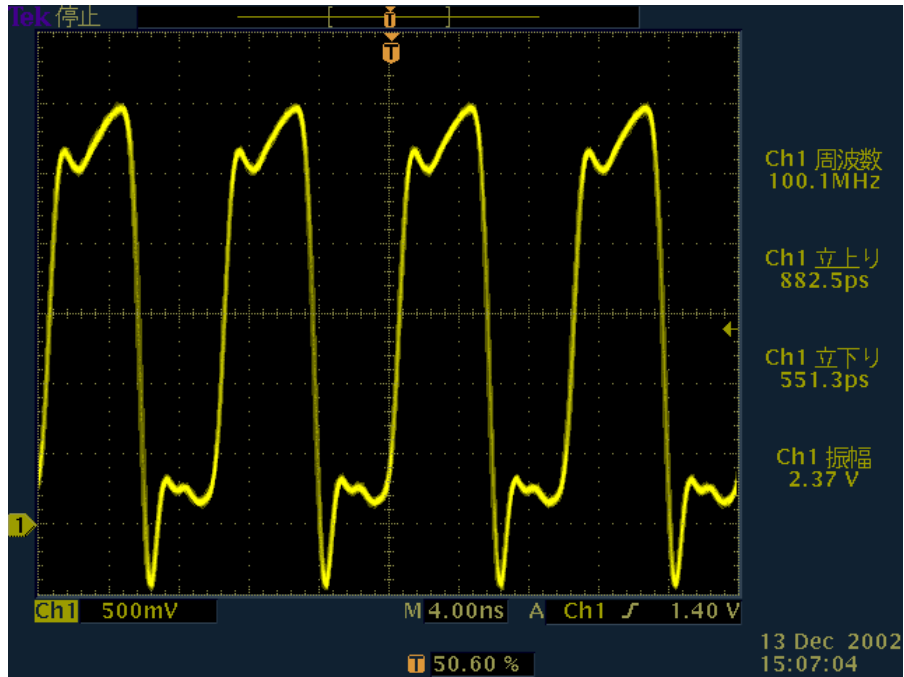
With Load @ 25MHz



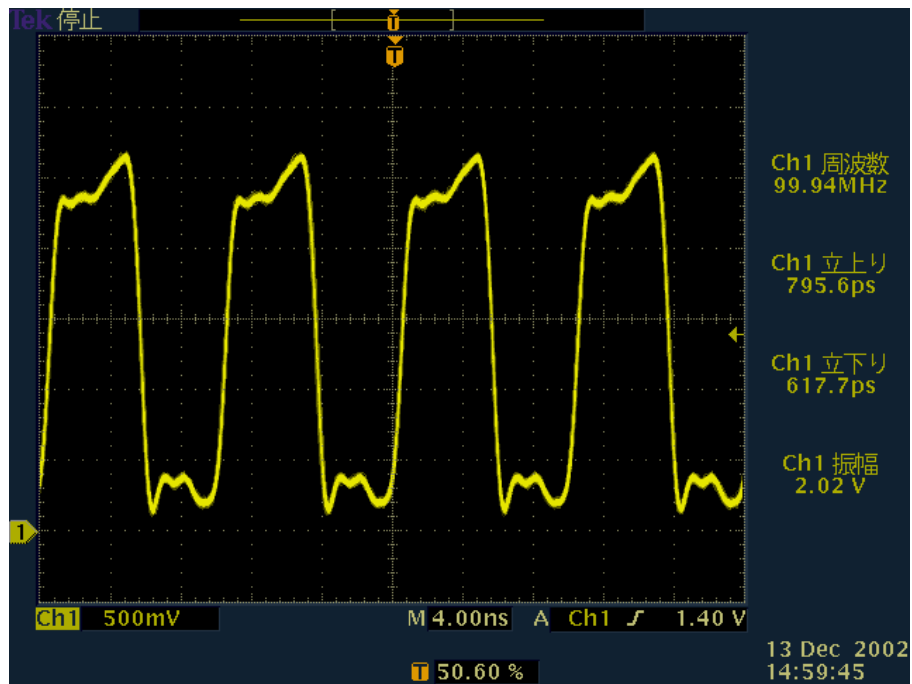
No Load @ 50MHz



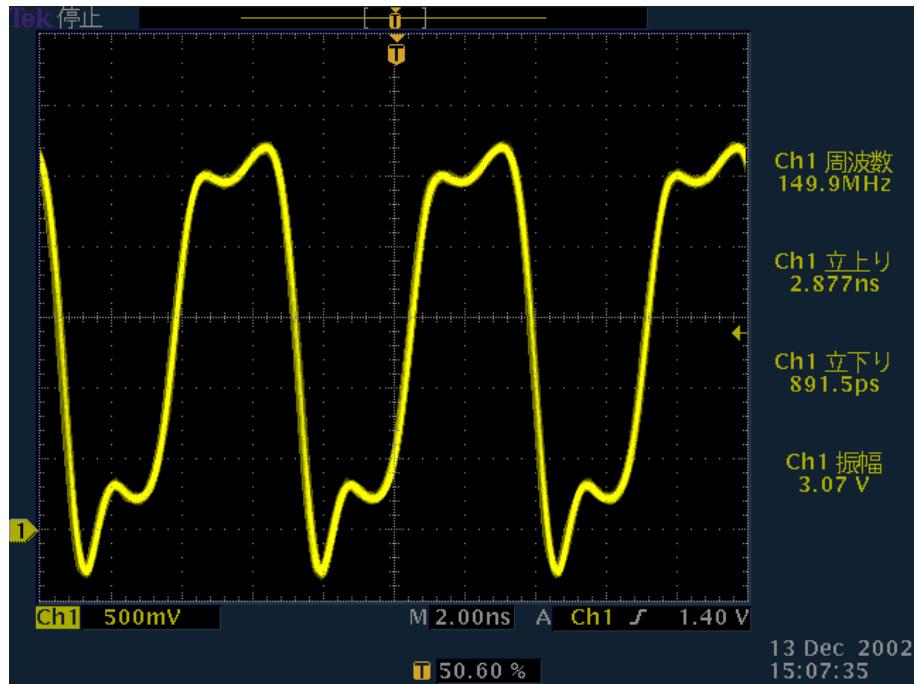
With Load @ 50MHz



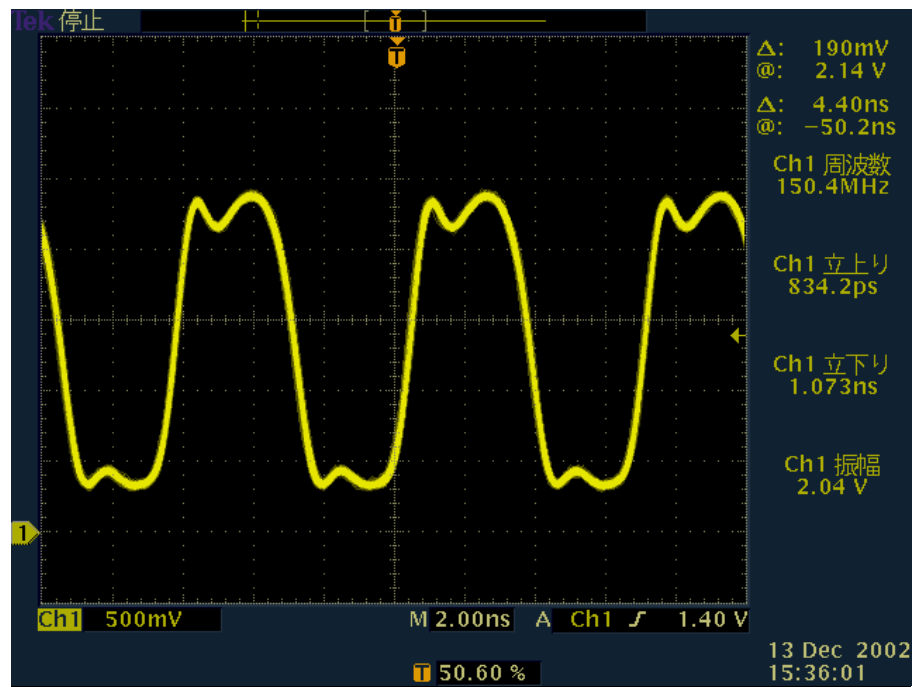
No Load @ 100MHz



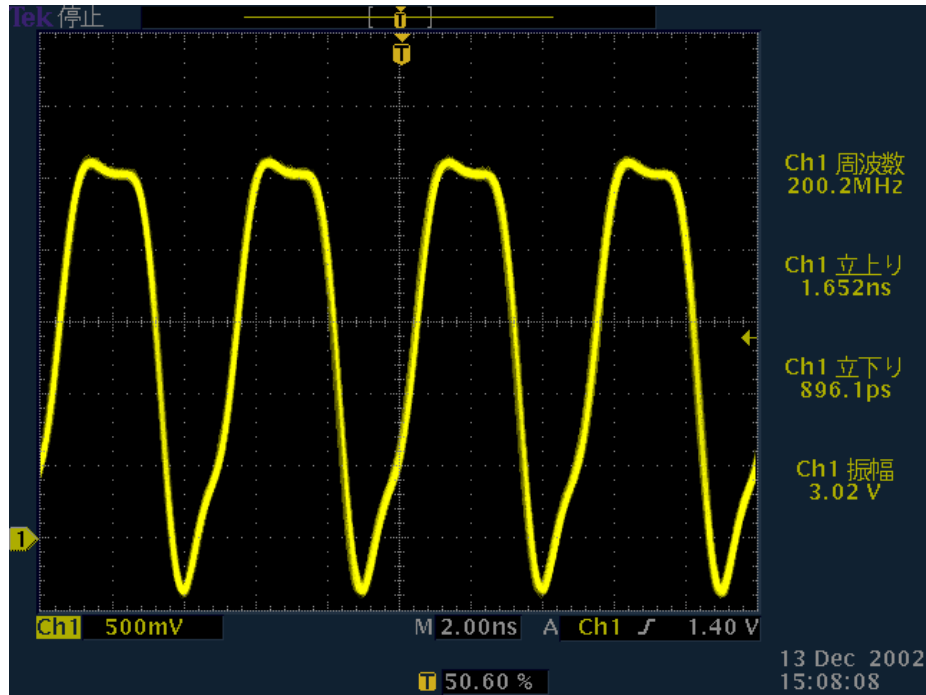
With Load @ 100MHz



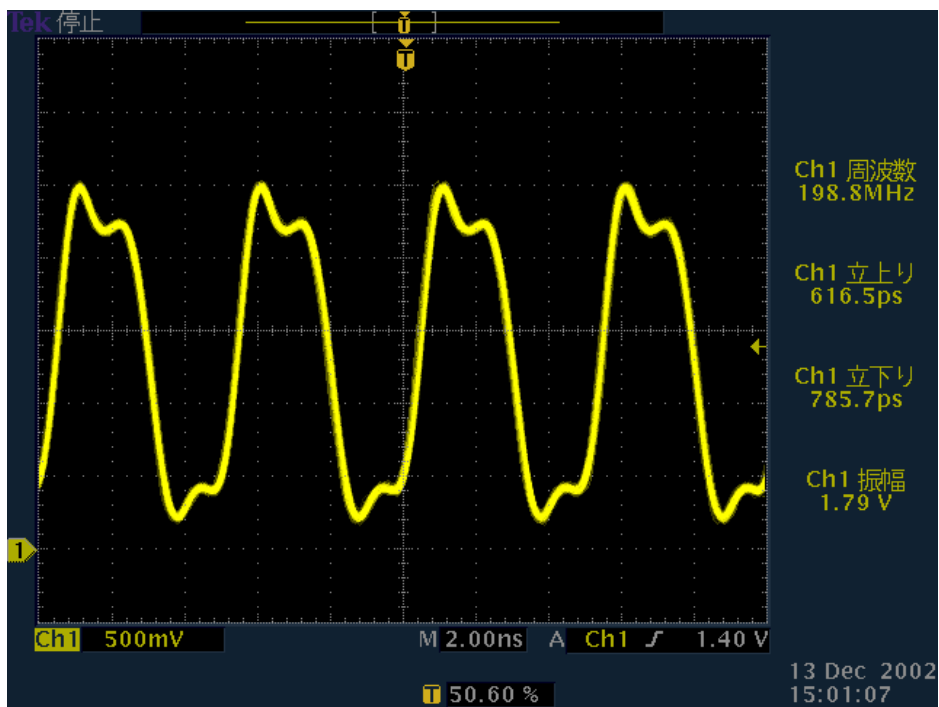
No Load @ 150MHz



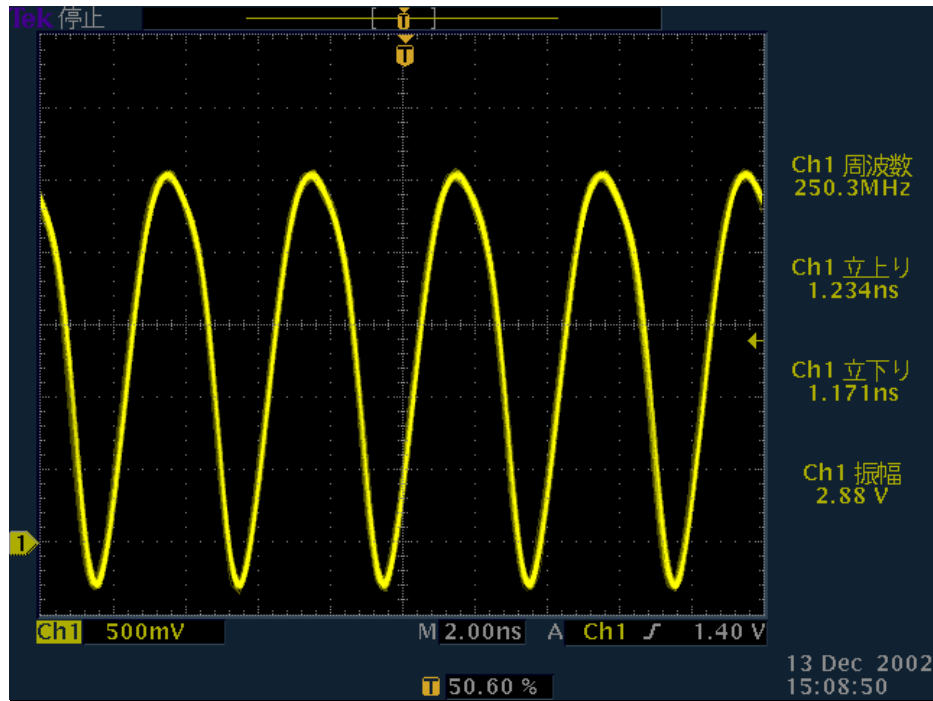
With Load @ 150MHz



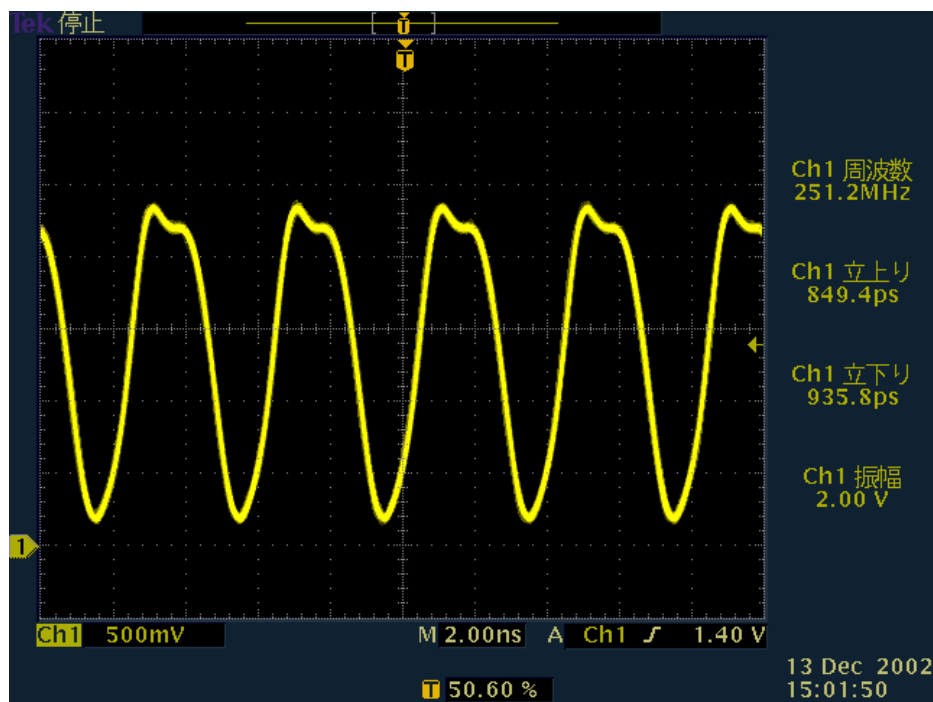
No Load @ 200MHz



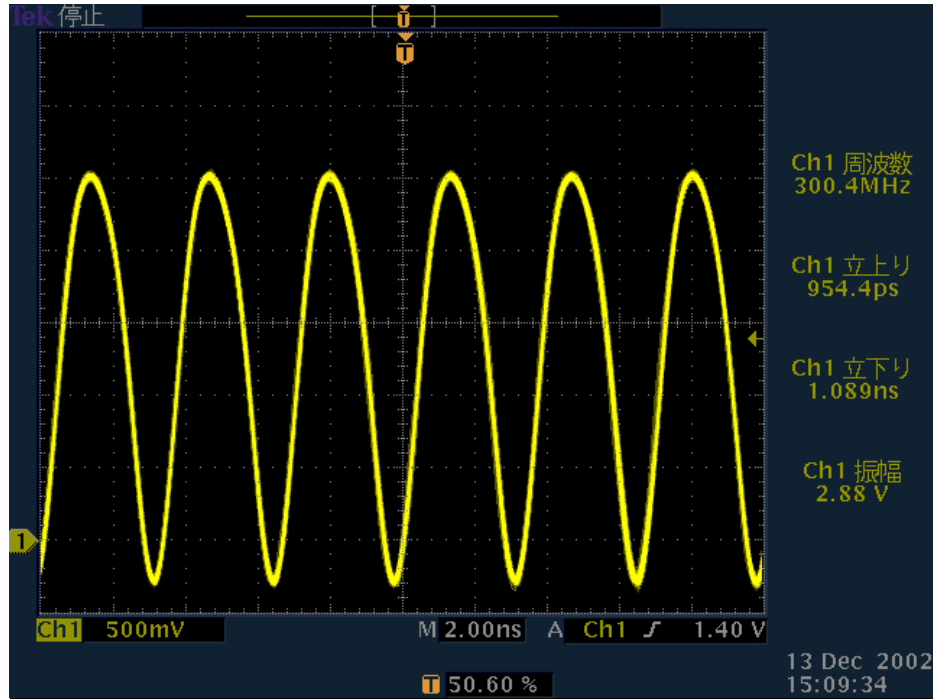
With Load @ 200MHz



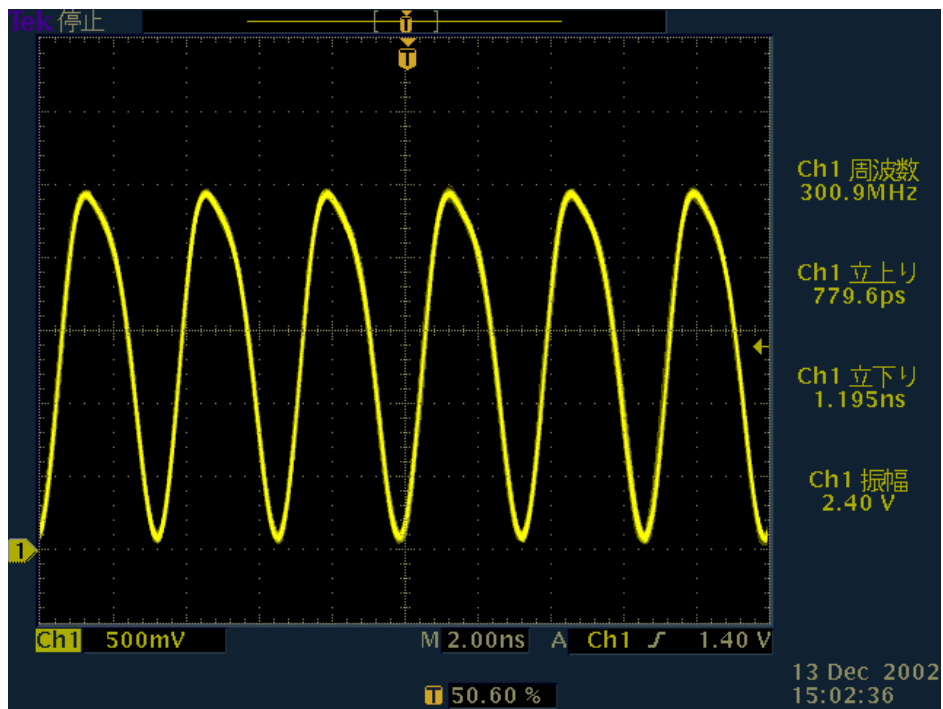
No Load @ 250MHz



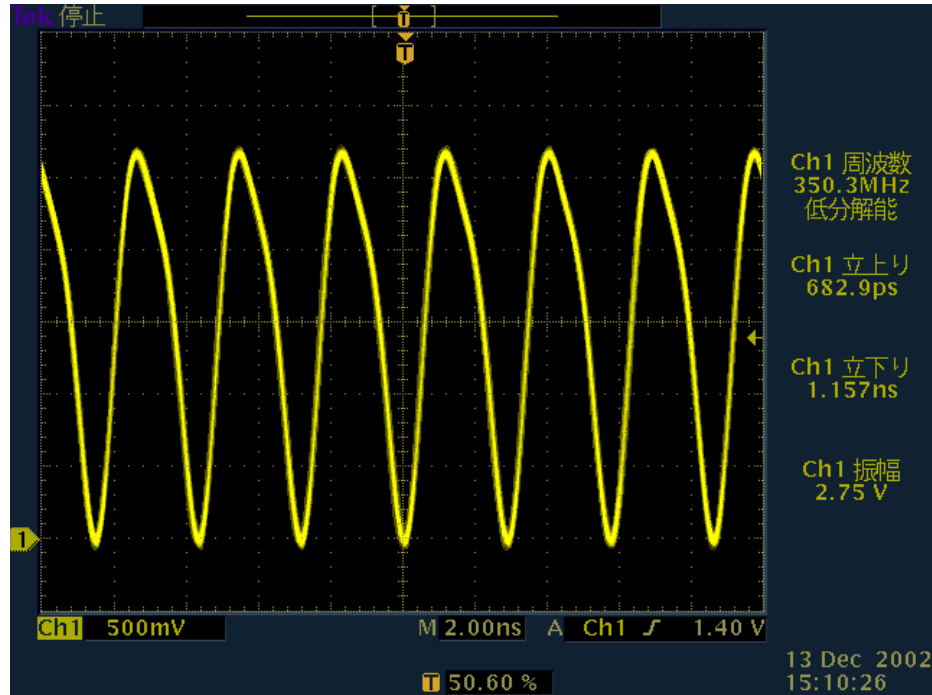
With Load @ 250MHz



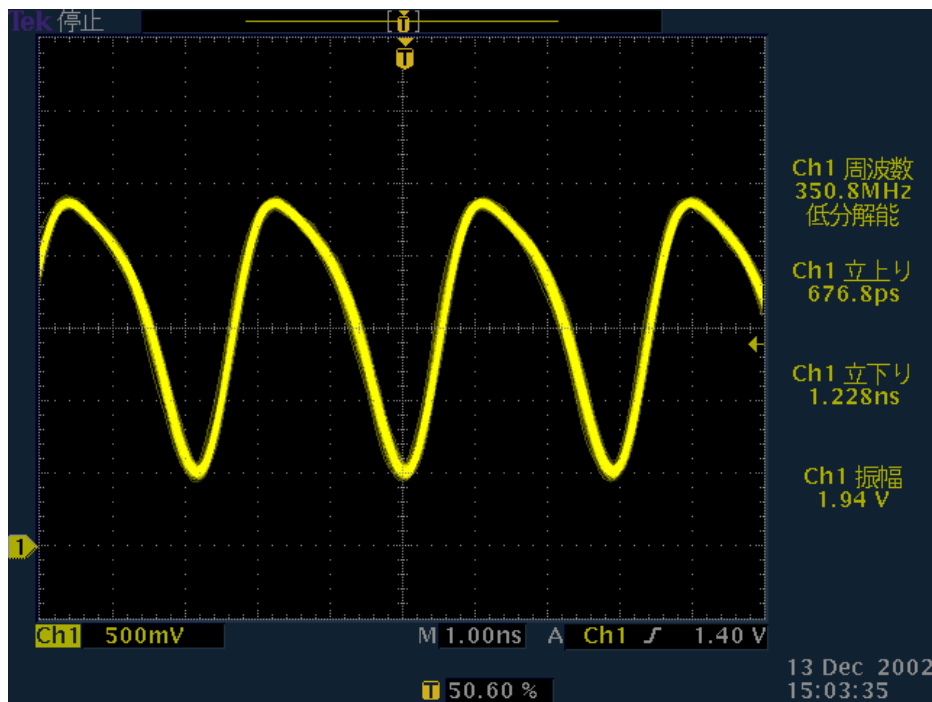
No Load @ 300MHz



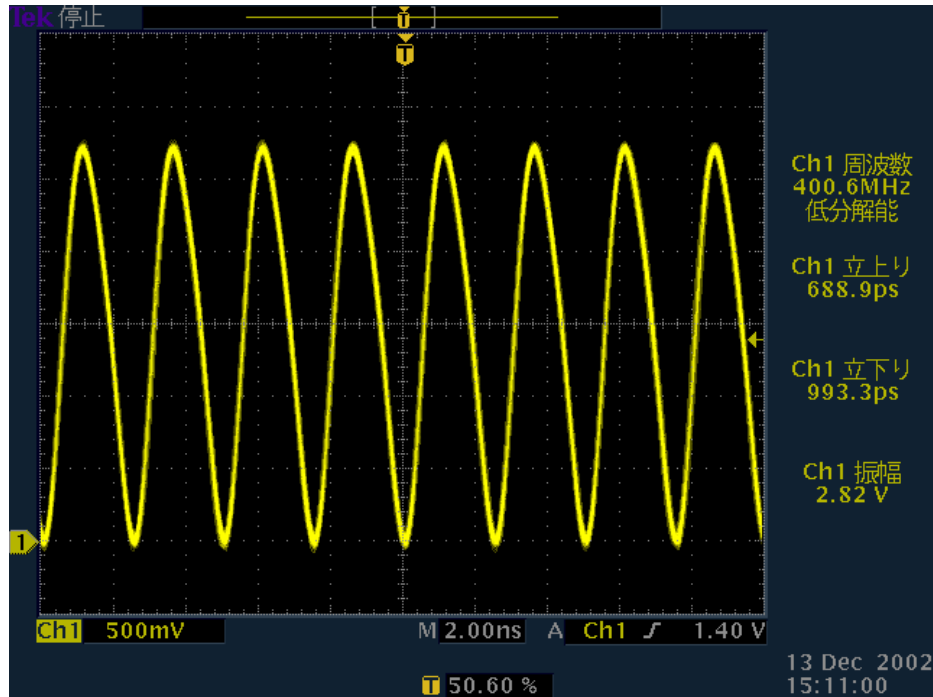
With Load @ 300MHz



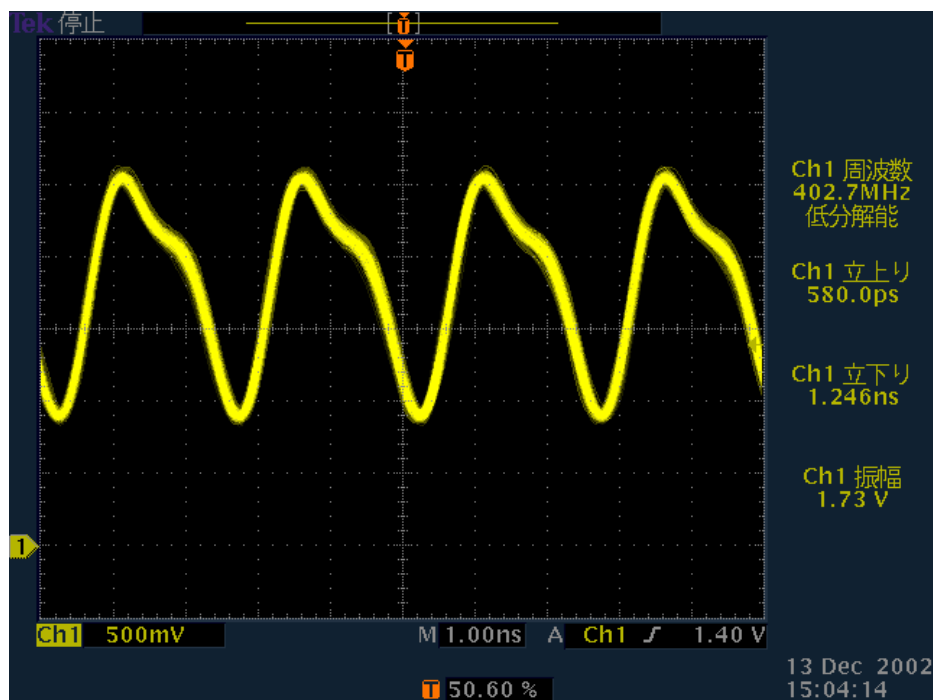
No Load @ 350MHz



With Load @ 350MHz

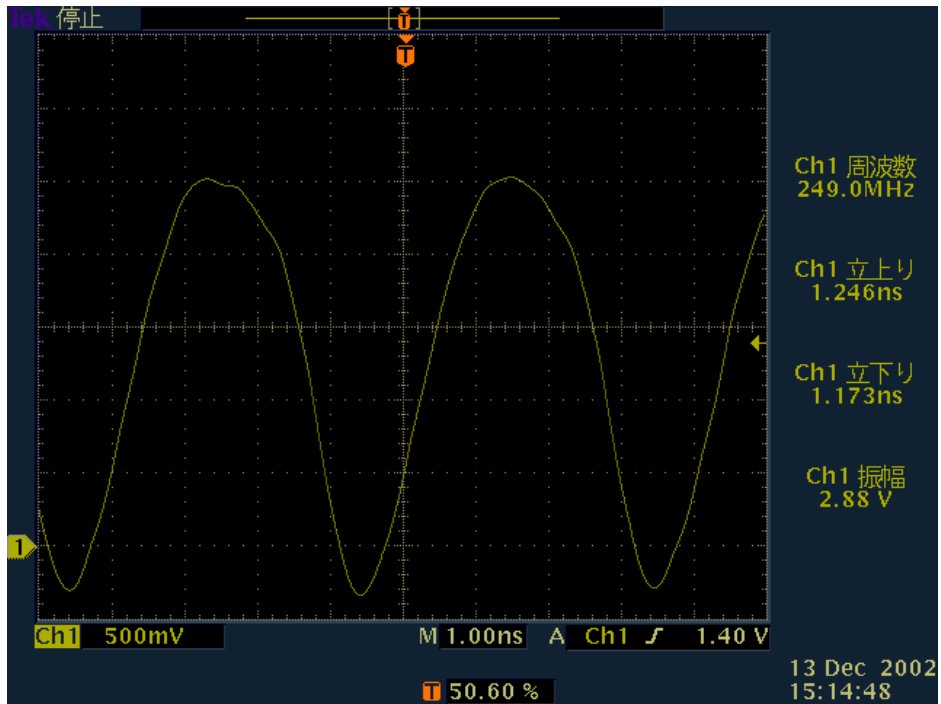


No Load @ 400MHz

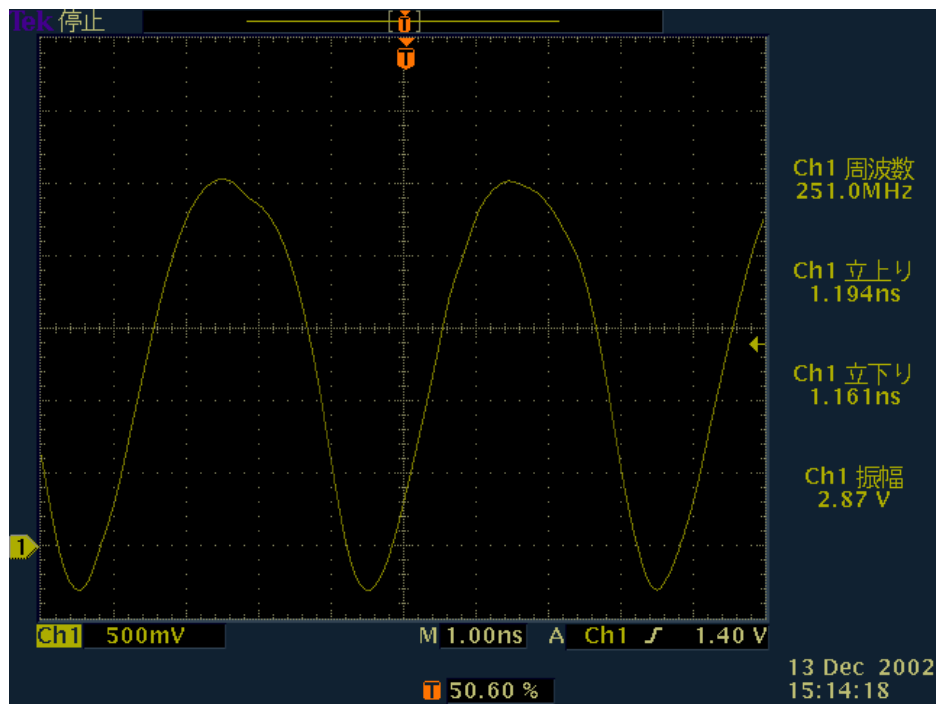


With Load @ 400MHz

[3] Result of Step Resolution around 250MHz Waveform



No Load @ 249MHz



No Load @ 251MHz